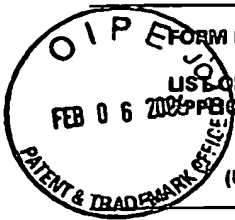


## PATENT APPLICATION

Sheet 1 of 6



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 LIST OF PATENTS AND PUBLICATIONS FOR  
 APPLICANT'S INFORMATION DISCLOSURE  
 STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

100111221-1

APPLICATION NO.

10/647,597

CONFIRMATION NO.

APPLICANT

S. Brandon Keller, et al.

FILING DATE

Aug. 25, 2003

GROUP

## REFERENCE DESIGNATION

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EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A			
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M-17	1Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,682, Entitled: Method And Program Product For Determining Nets Requiring Detailed Electromigration And Self Heating Analysis In A Digital Integrated Circuit; Filed November 12, 2003
1	1R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,698, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003
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## PATENT APPLICATION

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2Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,501, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit By Converting A Netlist To A DC Model And Performing DC Analysis Of The DC Model; Filed November 12, 2003
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CONFIRMATION NO.

U.S. DEPARTMENT OF COMMERCE  
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S. Brandon Keller, et al.

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5R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655555US; Docket: 200310170-1; Entitled: System And Method For Balancing Run-Time And Result Accuracy In A Circuit Design Analysis Tool; Filed January 30, 2004
5S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBERTS, George H.; U.S. Patent Application filed under EV210655578US; Docket: 200308900-1; Entitled: System And Method For Indicating Logic State Combinations Used During Circuit Design Analysis; Filed January 30, 2004

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DATE CONSIDERED

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100111221-1

APPLICATION NO.

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APPLICANT

S. Brandon Keller, et al.

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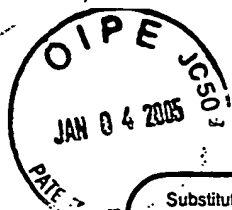
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PTO/SB/08a (08-03)

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Sheet 1 of 1

**Complete if Known**

Application Number	10/647,597
Filing Date	Aug. 25, 2003
First Named Inventor	S. Brandon Keller
Art Unit	
Examiner Name	
Attorney Docket Number	10011221-1

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
MJA	1110	US-2002/0010901 A1	01/24/2002	Otaguro	
		US- 6,185,722 B1	02/06/2001	Darden, et al.	
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		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
MJA	1110	JP 07334532 A	12/22/1995	Mitsubishi Denki Semiconductor		

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